

IO PINs

+5v		B2	+3.3v
PIN160	1	2	PIN152
PIN162	3	4	PIN161
PIN164	5	6	PIN163
PIN166	7	8	PIN165
PIN168	9	10	PIN167
PIN170	11	12	PIN169
PIN172	13	14	PIN171
PIN174	15	16	PIN173
PIN176	17	18	PIN175
PIN178	19	20	PIN177
PIN180	21	22	PIN179
PIN182	23	24	PIN181
PIN184	25	26	PIN183
PIN186	27	28	PIN185
PIN188	29	30	PIN187
PIN190	31	32	PIN189
PIN192	33	34	PIN191
PIN194	35	36	PIN193
PIN196	37	38	PIN195
PIN198	39	40	PIN197
GND			GND

Header 20x2

+5v		B1	+3.3v
PIN1	1	2	PIN2
PIN3	3	4	PIN4
PIN5	5	6	PIN6
PIN7	7	8	PIN8
PIN9	9	10	PIN10
PIN11	11	12	PIN12
PIN13	13	14	PIN14
PIN15	15	16	CLK_IN0
CLK_IN1	17	18	CLK_IN2
CLK_IN3	19	20	PIN30
PIN31	21	22	PIN32
PIN33	23	24	PIN34
PIN35	25	26	PIN36
PIN37	27	28	PIN38
PIN39	29	30	PIN40
PIN41	31	32	PIN42
PIN43	33	34	PIN44
PIN45	35	36	PIN46
PIN47	37	38	PIN48
GND	39	40	GND

Header 20x2

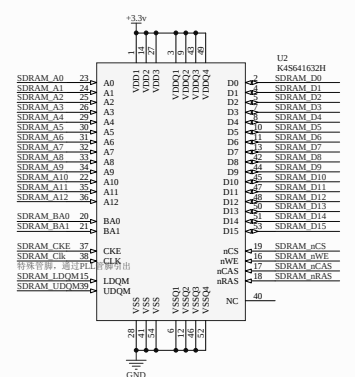
+5v		B4	+3.3v
PIN57	1	2	PIN56
PIN59	3	4	PIN58
PIN61	5	6	PIN60
PIN63	7	8	PIN62
PIN65	9	10	PIN64
PIN67	11	12	PIN66
PIN69	13	14	PIN68
PIN71	15	16	PIN70
PIN73	17	18	PIN72
PIN75	19	20	PIN74
PIN77	21	22	PIN76
PIN79	23	24	PIN78
PIN81	25	26	PIN80
PIN83	27	28	PIN82
PIN85	29	30	PIN84
PIN87	31	32	PIN86
PIN89	33	34	PIN88
PIN91	35	36	PIN90
PIN93	37	38	PIN92
PIN95	39	40	PIN94
PIN97			PIN95
PIN99			PIN97
PIN101			PIN99
PIN103			PIN101
GND			GND

Header 20x2

+5v		B3	+3.3v
PIN105	1	2	PIN106
PIN107	3	4	PIN108
PIN109	5	6	PIN110
PIN111	7	8	PIN112
PIN113	9	10	PIN114
PIN115	11	12	PIN116
PIN117	13	14	PIN118
PIN119	15	16	CLK_IN7
CLK_IN8	17	18	CLK_IN9
CLK_IN6	19	20	CLK_IN5
CLK_IN4	21	22	PIN126
PIN128	23	24	PIN129
PIN130	25	26	PIN131
PIN132	27	28	PIN133
PIN134	29	30	PIN135
PIN136	31	32	PIN137
PIN138	33	34	PIN139
PIN140	35	36	PIN141
PIN142	37	38	PIN143
PIN144	39	40	PIN145
PIN146			PIN147
PIN148			PIN149
PIN150			PIN151
GND			GND

Header 20x2

SDRAM



FPGA

U1A		PIN1	ASDI_AS	As input tri-stated
IO, (ASDI)		PIN2	ASDI_AS <td>As input tri-stated</td>	As input tri-stated
IO, (nCS0)		PIN3	SRAM_A0	SDRAM_A3
IO, LVDS0p, (CBE, FERR0)		PIN4	SRAM_A8	SDRAM_A2
IO, LVDS0s, (CLKUSR)		PIN5	SRAM_A7	SDRAM_A1
IO, LVDS0p		PIN6	SRAM_A6	SDRAM_A0
IO, LVDS0s		PIN7	SRAM_A5	SDRAM_A10
IO, LVDS0p, DQ10L		PIN8	SRAM_A4	SDRAM_BA1
IO, LVDS0s, DQ11L		PIN9	SRAM_A3	SDRAM_BA0
IO, LVDS0p, DQ12L		PIN10	SRAM_A2	SDRAM_nCS
IO, LVDS0s, DQ13L		PIN11	SRAM_A1	SDRAM_nCAS
IO, VREFB100		PIN12	SRAM_R06	SDRAM_nCAS
IO, LVDS0p, DQ14L		PIN13	SRAM_R5	SDRAM_nCAS
IO, LVDS0s, DQ15L		PIN14	SRAM_R4	SDRAM_nCAS
IO, LVDS0p, (DPCLK0/DQS0L)/(DPCLK0/DQS0L)		PIN15	SRAM_R3	SDRAM_nCAS
IO, LVDS0s, (DPCLK0/DQS0L)/(DPCLK0/DQS0L)		PIN16	SRAM_R2	SDRAM_nCAS
IO, LVDS0p		PIN17	SRAM_R1	SDRAM_nCAS
IO, LVDS0s		PIN18	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, (DPCLK1/DQS1L)/(DPCLK1/DQS1L)		PIN19	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, (DPCLK1/DQS1L)/(DPCLK1/DQS1L)		PIN20	SRAM_R0	SDRAM_nCAS
IO, LVDS0p		PIN21	SRAM_R0	SDRAM_nCAS
IO, LVDS0s		PIN22	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ16L		PIN23	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ17L		PIN24	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ18L		PIN25	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ19L		PIN26	SRAM_R0	SDRAM_nCAS
IO, VREFB101		PIN27	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ20L		PIN28	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ21L		PIN29	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ22L		PIN30	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ23L		PIN31	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ24L		PIN32	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ25L		PIN33	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ26L		PIN34	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ27L		PIN35	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ28L		PIN36	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ29L		PIN37	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ30L		PIN38	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ31L		PIN39	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ32L		PIN40	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ33L		PIN41	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ34L		PIN42	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ35L		PIN43	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ36L		PIN44	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ37L		PIN45	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ38L		PIN46	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ39L		PIN47	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ40L		PIN48	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ41L		PIN49	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ42L		PIN50	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ43L		PIN51	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ44L		PIN52	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ45L		PIN53	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ46L		PIN54	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ47L		PIN55	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ48L		PIN56	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ49L		PIN57	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ50L		PIN58	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ51L		PIN59	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ52L		PIN60	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ53L		PIN61	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ54L		PIN62	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ55L		PIN63	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ56L		PIN64	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ57L		PIN65	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ58L		PIN66	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ59L		PIN67	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ60L		PIN68	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ61L		PIN69	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ62L		PIN70	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ63L		PIN71	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ64L		PIN72	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ65L		PIN73	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ66L		PIN74	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ67L		PIN75	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ68L		PIN76	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ69L		PIN77	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ70L		PIN78	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ71L		PIN79	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ72L		PIN80	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ73L		PIN81	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ74L		PIN82	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ75L		PIN83	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ76L		PIN84	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ77L		PIN85	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ78L		PIN86	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ79L		PIN87	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ80L		PIN88	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ81L		PIN89	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ82L		PIN90	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ83L		PIN91	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ84L		PIN92	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ85L		PIN93	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ86L		PIN94	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ87L		PIN95	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ88L		PIN96	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ89L		PIN97	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ90L		PIN98	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ91L		PIN99	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ92L		PIN100	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ93L		PIN101	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ94L		PIN102	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ95L		PIN103	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ96L		PIN104	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ97L		PIN105	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ98L		PIN106	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ99L		PIN107	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ100L		PIN108	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ101L		PIN109	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ102L		PIN110	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ103L		PIN111	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ104L		PIN112	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ105L		PIN113	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ106L		PIN114	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ107L		PIN115	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ108L		PIN116	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ109L		PIN117	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ110L		PIN118	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ111L		PIN119	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ112L		PIN120	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ113L		PIN121	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ114L		PIN122	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ115L		PIN123	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ116L		PIN124	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ117L		PIN125	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ118L		PIN126	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ119L		PIN127	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ120L		PIN128	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ121L		PIN129	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ122L		PIN130	SRAM_R0	SDRAM_nCAS
IO, LVDS0s, DQ123L		PIN131	SRAM_R0	SDRAM_nCAS
IO, LVDS0p, DQ124L		PIN132	SRAM_R0	SDRAM_nCAS