

ATtiny48/88 is pin-compatible with ATmega48/88/168 and ATmega48P/88P/168P/328P by default, although some pin functions have been removed and other functions have been added. These changes should not affect pin compatibility by default but have been listed in the table below for clarity.

Table 3-2. Pin configurations.

Por Defecto Como Lo Pusiste

2	☒ CKSEL2 Select Clock Source	☐ BODLEVEL2 Brown-out Detector trigger level	☐ BOOTSZ1 Select boot size
1	☐ CKSEL1 Select Clock Source	☐ BODLEVEL1 Brown-out Detector trigger level	☒ BOOTSZ0 Select boot size
0	☒ CKSEL0 Select Clock Source	☐ BODLEVEL0 Brown-out Detector trigger level	☒ BOOTRST Select reset vector

Apply manual fuse bit settings

Current settings

These fields show the actual hexadecimal representation of the fuse settings from above. These are the values you have to program into your AVR device. Optionally, you may fill in the numerical values yourself to preset the configuration to these values. Changes in the value fields are applied instantly (taking away the focus)!

Low	High	Extended	Action	AVRDUDE arguments
0x62	0xDF	0xFC*	Apply values Defaults	-U lfuse:w:0x62:m -U hfuse:w:0xdf:m -U efuse:w:0xfc:m
Apply manual changes to the values on the left side, or load factory default values for the selected device.				Select (try triple-click) and copy-and-paste this option string into your avrdude command line. You may specify multiple -U arguments within one call of avrdude.
				* Note that some numerical values refer to fuses containing undefined bits (set to '1' here). Depending on the target device these fuse bits will be read either as '0' or '1'. Verification errors will occur if the values are read back with undefined bits set to '0'. Everything is fine if the values read from the device are either the same as programmed, or the following values (undefined set to '0'): <u>Extended: 0x04</u> .

Como Debería De Ser

Select Clock Source		EEPROM memory is preserved through chip erase		
2	☒ CKSEL2 Select Clock Source	☐ BODLEVEL2 Brown-out Detector trigger level	☐ BOOTSZ1 Select boot size	
1	☐ CKSEL1 Select Clock Source	☐ BODLEVEL1 Brown-out Detector trigger level	☒ BOOTSZ0 Select boot size	
0	☒ CKSEL0 Select Clock Source	☐ BODLEVEL0 Brown-out Detector trigger level	☒ BOOTRST Select reset vector	

Apply manual fuse bit settings

Current settings

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Low	High	Extended	Action	AVRDUDE arguments
0x62	0xDF	0x04	Apply values Defaults	-U lfuse:w:0x62:m -U hfuse:w:0xdf:m -U efuse:w:0x04:m
Apply manual changes to the values on the left side, or load factory default values for the selected device.				Select (try triple-click) and <u>copy-and-paste this option string into your avrdude</u> command line. You may specify multiple -U arguments within one call of avrdude.